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## APPLICANTS

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\*\* CONTINUING DATA ..... *None*

\*\* FOREIGN APPLICATIONS ..... *yes*  
 GERMANY 100 37 794.7 08/03/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 09/10/2001

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY GERMANY	SHEETS DRAWING 13	TOTAL CLAIMS 28	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>PMC</i> Initials				

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## TITLE

Method and device for testing an integrated circuit, integrated circuit to be tested, and wafer with a large number of integrated circuits to be tested

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